

AMENDMENTS TO THE CLAIMS

The listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A method for debugging software code, said method comprising:

initiating a debugger thread on a computer system, wherein the debugger thread performs a plurality of debugger events;

invoking an operational thread on the computer system using the debugger thread, the operational thread performing operational tasks, wherein the invoking starts the operational thread for a first time;

executing the software code using the operational thread;

detecting whether the operational thread is functioning using the debugger thread; and

debugging the operational thread using the debugger thread in response to the detecting.
2. (Original) The method as described in claim 1 further comprising:

receiving a request from a software developer;

retrieving operational data from the operational thread using the debugger thread in response to the request; and

providing the operational data to the developer.

3. (Original) The method as described in claim 2 wherein the operational data is selected from the group consisting of a register value, a code line failure value, an interrupt value, and a memory value.
4. (Original) The method as described in claim 2 wherein the operational data is one or more register values, and wherein the register locations are different from debugger register locations that are used by the debugger thread.
5. (Original) The method as described in claim 1 further comprising:
loading a debugger operating system on the debugger thread; and
loading a primary operating system on the operational thread, wherein the debugger operating system is different from the primary operating system.
6. (Original) The method as described in claim 1 wherein the computer system includes a plurality of dissimilar processors, wherein the operational thread and the debugger thread both execute on a common dissimilar processor from the plurality of dissimilar processors.
7. (Original) The method as described in claim 2 wherein the dissimilar processor is a processing unit.
8. (Currently Amended) An information handling system comprising:
a processor;
a memory accessible by the processor;
one or more nonvolatile storage devices accessible by the processor; and
a debugger thread tool for debugging an operational thread, the debugger thread tool comprising software code effective to:

initiate a debugger thread on a computer system, wherein the debugger thread performs a plurality of debugger events;

invoke an operational thread on the computer system using the debugger thread, the operational thread performing operational tasks, wherein the invoking starts the operational thread for a first time;

execute the software code using the operational thread;

detect whether the operational thread is functioning using the debugger thread; and

debug the operational thread using the debugger thread in response to the detecting.

9. (Original) The information handling system as described in claim 8 wherein the software code is further effective to:
 - receive a request from a software developer;
 - retrieve operational data from the operational thread using the debugger thread in response to the request; and
 - provide the operational data to the developer.
10. (Original) The information handling system as described in claim 9 wherein the operational data is selected from the group consisting of a register value, a code line failure value, an interrupt value, and a memory value.
11. (Original) The information handling system as described in claim 9 wherein the operational data is one or more register values, and wherein the register locations are different from debugger register locations that are used by the debugger thread.

12. (Original) The information handling system as described in claim 8 wherein the software code is further effective to:
- load a debugger operating system on the debugger thread; and
- load a primary operating system on the operational thread, wherein the debugger operating system is different from the primary operating system.
13. (Original) The information handling system as described in claim 8 wherein the computer system includes a plurality of dissimilar processors, wherein the operational thread and the debugger thread both execute on a common dissimilar processor from the plurality of dissimilar processors.
14. (Currently Amended) A computer program product stored on a computer operable media for debugging an operational thread using a debugger thread, said computer program product comprising:
- means for initiating a debugger thread on a computer system, wherein the debugger thread performs a plurality of debugger events;
- means for invoking an operational thread on the computer system using the debugger thread, the operational thread performing operational tasks, wherein the invoking starts the operational thread for a first time;
- means for executing the software code using the operational thread;
- means for detecting whether the operational thread is functioning using the debugger thread; and
- means for debugging the operational thread using the debugger thread in response to the detecting.
15. (Original) The computer program product as described in claim 14 further comprising:

means for receiving a request from a software developer;

means for retrieving operational data from the operational thread using the debugger thread in response to the request; and

means for providing the operational data to the developer.

16. (Original) The computer program product as described in claim 15 wherein the operational data is selected from the group consisting of a register value, a code line failure value, an interrupt value, and a memory value.
17. (Original) The computer program product as described in claim 15 wherein the operational data is one or more register values, and wherein the register locations are different from debugger register locations that are used by the debugger thread.
18. (Original) The computer program product as described in claim 14 further comprising:

means for loading a debugger operating system on the debugger thread; and

means for loading a primary operating system on the operational thread, wherein the debugger operating system is different from the primary operating system.
19. (Original) The computer program product as described in claim 14 wherein the computer system includes a plurality of dissimilar processors, wherein the operational thread and the debugger thread both execute on a common dissimilar processor from the plurality of dissimilar processors.
20. (Original) The computer program product as described in claim 15 wherein the dissimilar processor is a processing unit.